

IN THE UNITED STATES PATENT OFFICE

Applicants: Kevin T. Look et al.

Assignee: Xilinx, Inc.

Title: Regulating Unused/Inactive Resources in Programmable Logic Devices for Static Power Reduction

Serial No.: 10/783,589 File Date: February 20, 2004

Examiner: Unknown Art Unit: 2800

Docket No.: X-1462-2P US Conf. No.: 1594

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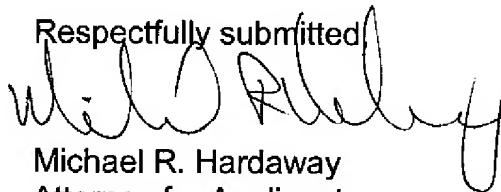
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, Applicants bring to the attention of the Examiner the reference listed in the attached PTO/SB/08A form.

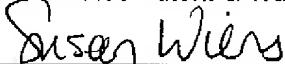
Citation of the listed reference shall not be construed as an admission that the reference is necessarily prior art with respect to the instant invention. Citation of the listed reference shall not be construed as a representation that a search has been made other than as described above. Also, citation of the listed reference shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

The cited reference is being disclosed prior to the receipt of a first Office action. Accordingly, it is believed that no filing fee is due. However, the Commissioner is authorized to charge any fees which may be required for this matter, or credit any overpayment, to Deposit Account No. 24-0040.

Respectfully submitted  
  
Michael R. Hardaway

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I hereby certify that this correspondence is being filed via EFS-Web with the United States Patent & Trademark Office on April 21, 2008.

  
Typed Name: Susan Wiens